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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner: Quang D. Vu

Art Unit: 2811

In Re Application of:

Arash Hassibi, et al.

Application No: 09/843,486

Filed: April 25, 2001

For: Optimal Simultaneous Design and

Floorplanning Of Integrated

Circuit

Assistant Commissioner For Patents

Washington, D.C. 20231

AMENDMENT AND RESPONSE

Sir:

In response to the Office Action mailed August 7, 2002 please enter the following amendments and consider the following remarks.

IN THE DRAWINGS

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

on <u>i)ecember 9,2002</u> Date of Deposit

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